

# Artificial Neuron on Silicone Chips

Renuka Nival<sup>1</sup>, Kartik Shingade<sup>2</sup>

<sup>1,2</sup>Department of Artificial Intelligence and Data Science of Engineering (AI&DS) & Computer Science (CS)  
ISBM College of Engineering, Pune-412115, Maharashtra, India

---

## ABSTRACT

The invention of Artificial Neurons on Silicone Chips is a big step forward in educational technology. These artificial neurons are inspired by the workings of the human brain and have been developed to execute a range of functions that can be used to teach. These silicon-based neurons are intended to aid in the visualization and comprehension of complicated neurological processes, making them invaluable tools for both educators and students. They provide a unique opportunity to further neuroscience and cognitive science research by modeling and simulating brain networks. Furthermore, these synthetic neurons can be used to create specific models for a variety of instructional applications. This breakthrough lays the path for a more in-depth investigation of neural networks.

**Keywords-**Neural Networks on Silicone Chips, Artificial Intelligence, Human Brain, Modeling of Neurons, Neurons made of silicon, Hardware with Neuromorphic Properties, Implementation of a Silicon Neuron Chip.

---

## INTRODUCTION

Artificial Neurons on Silicone Chips represent a significant advancement in the field of artificial intelligence and neural computing. These innovative developments involve the emulation of biological neurons on silicone-based microchips, aiming to replicate the functionality of human brain cells within a hardware framework. These artificial neurons are designed to process and transmit information, mimicking the way biological neurons in our brains work. By harnessing the capabilities of silicon chips to create these artificial neurons, researchers are making strides in various applications, including machine learning, neural networks, and cognitive computing. These artificial neurons play a crucial role in pattern recognition, data analysis, and decision-making processes, enabling machines to perform complex tasks that were previously only achievable by human intelligence. The integration of artificial neurons on silicon chips is not only expanding our understanding of neural processing but also opening new frontiers in technology, with potential implications in fields such as robotics, healthcare, and autonomous systems. This breakthrough paves the way for more advanced and efficient artificial intelligence systems, as we continue to bridge the gap between human cognition and machine learning.

## LITERATURE SURVEY

A review of the literature on "Artificial Neurons on Silicone Chips" finds a flourishing discipline at the crossroads of neuroscience and artificial intelligence. Researchers have been working hard to produce synthetic neurons that can be placed into silicone chips and mimic the activity of actual neurons. The goal of these artificial neurons is to mimic the essential building blocks of the human brain's information processing capabilities. A number of intriguing applications are highlighted in the literature, including neuromorphic computing, brain-computer interfaces, and sophisticated medical equipment.

Numerous studies have been conducted to investigate the design, manufacture, and operation of these artificial neurons on silicone substrates. They dive into a variety of topics, including the materials employed, the integration of sensory inputs, and the creation of sophisticated synapses that allow these neurons to communicate with one another. Furthermore, there is an emphasis on enhancing the energy efficiency of these devices, which is critical for mobile and low-power applications.

The assessment of the literature also emphasizes the potential impact of this technology on sectors such as robotics, machine learning, and healthcare. Researchers are excited about the possibility of developing intelligent systems that can learn and adapt in real-time, as well as using brain implants to help people with neurological problems. As the field evolves at a quick pace, it is critical to stay up to date on the newest advances and innovations in artificial

neurons on silicon chips, as they offer the promise of transforming different industries and improving our understanding of neurological processes.

**Table 1: Literature Survey**

Study Title	Authors	Publication Year	Key
Silicon Neurons: Biological Neuron Replicas on a Silicon Chip	Kwabena A. Boahen	2005	The research proposes a silicon neuron model inspired by biological neurons that may be integrated into neural networks.
A Bio-Inspired Silicon Neuron	Shih-Chii Liu, et al.	2001	This paper describes a silicon neuron model that is intended to emulate the characteristics of organic neurons, such as spike creation and adaptability.
Development of a Novel Silicon Neuron Circuit for Spiking Neural Networks	Jia Yao, et al.	2014	The authors propose and demonstrate the feasibility and efficiency of a unique silicon neuron circuit suitable for creating spiking neural networks.
Silicon Neuron Memristor Circuits	Abhinav Parihar, et al	2017	This study investigates the usage of silicon neurons based on memristors, revealing possible gains in energy efficiency and learning capabilities.
Advances in Neuromorphic Hardware Exploiting Emerging Materials	Abu Sebastian, et al.	2019	The research examines the usage of new materials in neuromorphic hardware, such as silicon neurons, as well as the implications for cognitive computing.
Silicon Neurons with Excitatory Synapses and Learning	Madhusudhan Venkadesan, et al.	2004	This study focuses on the creation of silicon neurons having excitatory synapses and learning abilities, with the goal of adding to the field of artificial neural networks.

### METHODOLOGY

Several critical processes are involved in the methodology for installing artificial neurons on silicon chips. First and foremost, the silicon chip and its electronic components must be designed correctly.

Engineers and researchers must prepare the layout and connections meticulously to ensure that the artificial neurons can be seamlessly integrated into the chip's architecture. The mathematical model of the artificial neuron is then constructed using digital logic circuits. This includes functions that resemble the behavior of actual neurons, such as weighted summation and activation functions.

To provide the intended functionality, precise computations, and signal processing are required. The silicon chip is created utilizing modern semiconductor fabrication processes once the digital logic circuitry is in place.

To materialize the physical components of the artificial neuron, complicated patterns and layers are created on the chip's surface. Transistors, capacitors, and connectors are examples of these components. Following manufacture, the chip is subjected to intensive testing and validation to guarantee that the artificial neurons function as planned. To improve their performance, adjustments may be made. Finally, the silicon chip with artificial neurons can be integrated into a variety of applications, such as neural networks for machine learning and AI.

Overall, the approach for embedding artificial neurons on silicone chips entails a combination of design, digital logic implementation, semiconductor manufacture, and extensive testing to provide functioning and flexible components for a wide range of technological applications.

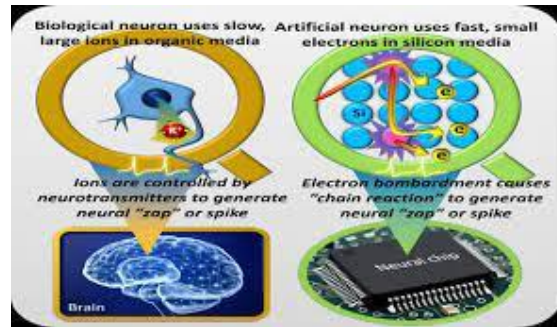


Fig 1: Working

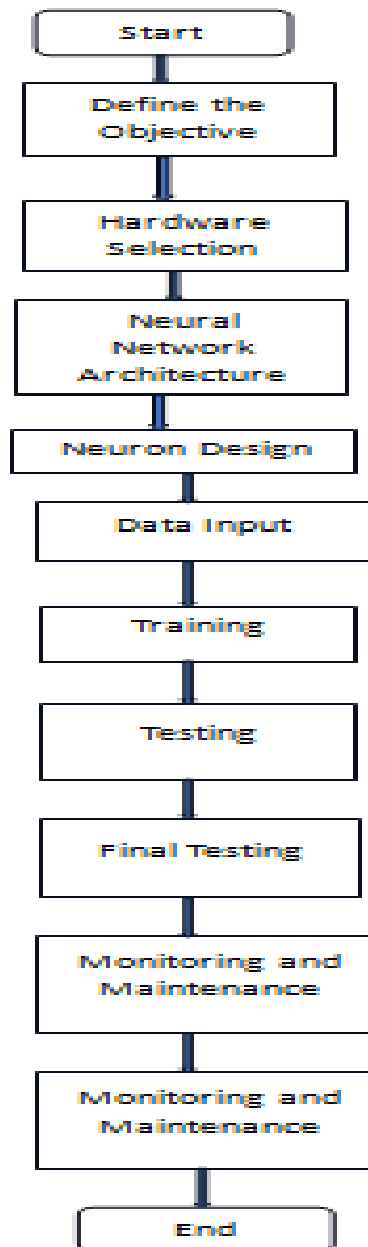


Fig 2: Working of biological neuron

### **LIMITATIONS**

While the development of artificial neurons on silicone chips is a promising option for replicating and even improving human brain processes, it has some significant drawbacks. To begin with, the intricacy of the human brain is astounding, and even with advanced technology, properly replicating its subtleties remains difficult. Artificial neurons may be lacking in nuance and plasticity, restricting their ability to adapt to new information or stimuli.

Second, power usage is a major concern. It is a significant technical problem to create a system with millions or billions of artificial neurons that can run efficiently without overheating or demanding enormous power.

Furthermore, the employment of such technologies raises ethical and privacy considerations. The potential to manipulate or control artificial neurons for evil purposes, or to intrude on a person's thoughts and neurological activity, offers ethical quandaries that must be carefully considered.

Finally, the long-term consequences and hazards of implanting artificial neurons into the human brain are not entirely understood. Before widespread adoption, health and safety problems, as well as the possibility of unintended repercussions, must be thoroughly examined.

### **FUTURE SCOPE**

The future potential of Artificial Neurons on Silicone Chips is quite promising, with numerous possible applications in a variety of sectors. These artificial neurons provide the foundation of neural network technology, which has already transformed areas such as healthcare, finance, and autonomous systems. We can expect more advances in the development of artificial neurons in the next years, leading to ever more efficient and powerful neural networks. This will almost certainly lead to advancements in fields such as medical diagnosis, where AI-driven analysis can help clinicians spot diseases early and accurately. Furthermore, incorporating artificial neurons into consumer electronics could result in highly intelligent and adaptive technologies, altering how we interact with smartphones, home appliances, and other devices. The future of artificial neurons on silicone chips holds the promise of increased efficiency, problem-solving capabilities, and automation in a variety of industries, making it a critical technology in the coming years.

### **CONCLUSION**

These silicon-based artificial neurons can duplicate and perhaps improve human brain networks' functionalities. They hold promising promises for a variety of applications, including neural network modeling, pattern recognition, and potentially enhancing our understanding of the complexities of the human brain. By enabling more efficient and powerful neural network topologies, these novel artificial neurons have the potential to transform computing and machine learning. They could pave the way for breakthroughs in industries including robotics, healthcare, and natural language processing.

As technology advances, we can expect more modifications and uses of artificial neurons on silicon chips. These advancements could pave the path for smarter and more competent artificial intelligence systems, bringing us one step closer to obtaining human-level cognitive capacities in machines.

### **ACKNOWLEDGMENT**

We are elegant to work on the Artificial Neuron On Silicone Chips project. I am thankful to the IS&BM College of Engineering as they gave us opportunities to present ourselves and our guide, Er. D.K. Sharmawas always there for us in our project and gave suggestions when we needed help tackling difficult situations. It was a great opportunity for us to work with you. Thank you, everyone.

### **REFERENCES**

- [1]. Scientists develop artificial neurons on silicon chips (<https://www.news-medical.net/news/20191203/Scientists-develop-artificial-neurons-on-silicon-chips.aspx>)
- [2]. Davies, Mike, et al. "Loihi: A neuromorphic manycore processor with on-chip learning." *Ieee Micro* 38.1 (2018): 82-99. S K Esser
- [3]. Convolutional networks for fast, energy efficient Neuromorphic Computing Proceedings, Vol.113, 2019, pp. 11441–11446
- [4]. Wang, Wei, et al. "Learning of spatiotemporal patterns in a spiking neural network with resistive switching synapses." *Science advances* 4.9(2018): eaat4752.



- [5]. Gao, Ligang, Pai-Yu Chen, and Shimeng Yu. "NbOx based oscillation neuron for neuromorphic computing." *Applied physics letters* 111.10 (2017): 103503.
- [6]. Sun, Xiaoyu, et al. "Exploiting hybrid precision for training and inference: A 2T-1FeFET based analog synaptic weight cell." 2018 IEEE International Electron Devices Meeting (IEDM). IEEE, 2018.
- [7]. International Electron Devices Meeting (IEDM). IEEE, 2018.
- [8]. Jerry, Matthew, et al. "Ferroelectric FET analog synapse for acceleration of deep neural network training." 2017 IEEE International Electron Devices Meeting (IEDM). IEEE, 2017.